

Electronic Patent Application Fee Transmittal

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|---|--|-----------------|---------------|-----------------------------|
| Application Number: | 10817201 | | | |
| Filing Date: | 02-Apr-2004 | | | |
| Title of Invention: | Variable threshold transistor for the Schottky FPGA and multilevel storage cell flash arrays | | | |
| First Named Inventor/Applicant Name: | Augustine W. Chang | | | |
| Filer: | Joseph A. Sawyer | | | |
| Attorney Docket Number: | 3065P | | | |
| Filed as Small Entity | | | | |
| Utility Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Extension-of-Time: | | | | |
| Extension - 2 months with \$0 paid | 2252 | 1 | 225 | 225 |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|-------------------|----------|----------|--------|----------------------|
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 225 |